

S/N 09/944,993

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Anthony Moschopoulos Examiner: James Peikari  
Serial No.: 09/944,993 Group Art Unit: 2187  
Filed: August 30, 2001 Docket: 703.155US1  
Confirmation No.: 1856  
Title: BIT INVERSION IN MEMORY DEVICES

COMMUNICATION CONCERNING RELATED APPLICATION

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11/460,371	July 27, 2006	703.155US2	BIT INVERSION IN MEMORY DEVICES

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications.

Respectfully submitted,

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Date 05-13-09

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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 13 day of May, 2009.

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